

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ABOT	(TK2P26)	Beginning of tape (BOT) detecting signal
ACEN	(RM3Q74)	
ACEN	(RM3N44)	Indicates that capstan motor is rotating
*ACEN	(RM2G64)	
ACT	(RG1J65)	
ADB00	(RG2B24)	
ADB01	(RG2B25)	Indicates the content of one from the upper 4 bits of the return address register of a subroutine
ADB02	(RG2B26)	
ADB03	(RG2B27)	
ADCRY	(RM1L54)	Indicates the carry bit of address counter
ADR00	(RM1K57)	
ADR01	(RM1K56)	
ADR02	(RM1K55)	
ADR03	(RM1K54)	
ADR04	(RM1F57)	
ADR05	(RM1F56)	Indicates the content of each bit from 0 to 11 address counter
ADR06	(RM1F55)	
ADR07	(RM1F54)	
ADR08	(RM1B57)	
ADR09	(RM1B56)	
ADR10	(RM1B55)	
ADR11	(RM1B54)	
*ADSET	(RM1N44)	Address preset signal

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
AEOT	(TK2P24)	Indicates that end of tape is detected
AGCOK	(RW2G66)	Self-adjusted gain control (SAGC) OK signal from read amplifier
AGCON *AGCON	(RG5E45) (RW5M24)	Indicates that the AGC operation is being performed
ACC8F	(RW4J94)	Indicates that the content of SAGC counter is greater than 8
AIRDV *AIRDV *AIR2	(RG1F65) (CB1D34) (CB1F34)	Directs the drive of air supply motor
ALAM0 ALAM1 ALAM2 ALAM3 *ALM0 *ALM1 *ALM1 *ALM2 *ALM2 *ALM3 *ALM3 *ALM3 *ALM4 *ALM5	(TK2C23) (TK2C24) (TK2C25) (TK2C26) (CB1K34) (CB1J34) (VQ1C26) (VQ1D24) (CB1H34) (CB1G34) (VQ1D25) (VQ1D26) (VQ1E23)	Alarm signal

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ALUOK	(RG1Q44)	Indicates that the Arithmetic and Logic Unit (ALU) operation is normal
ALUOK	(RG1G26)	
*ALUOK	(RG1Q34)	
ALUP	(ALLCY4)	Indicates the Arithmetic and Logic Unit (ALU) output bus bit 0 through 7 and parity
ALUP	(ALLBX6)	
ALUP	(ALLBX4)	
ALU0	(ALLG74)	Indicates Arithmetic and Logic Unit (ALU) output bus bit 0 through 7 and parity
ALU0	(ALLB63)	
ALU1	(ALLB64)	
ALU1	(ALLB74)	
ALU2	(ALLJ74)	
ALU2	(ALLB65)	
ALU3	(ALLK74)	
ALU3	(ALLB66)	
ALU4	(ALLG44)	
ALU4	(ALLG33)	
ALU5	(ALLB34)	
ALU5	(ALLH44)	
ALU6	(ALLJ44)	
ALU6	(ALLB35)	
ALU7	(ALLB36)	
ALU7	(ALLK44)	
ALWT	(TK2P25)	Indicates that low tape is detected

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
BGC	(SN1N24)	Backward GO signal
BK	(SN1C95)	
BK	(B15C94)	
*BKRUN	(TK1E24)	Indicates that capstan motor is rotating backward
BMD07	(MX1K44)	
BMD08	(RM2B34)	
*BMD08	(RM2B24)	
BMD09	(RM2D34)	
*BMD09	(RM2D24)	
BMD10	(RM2F34)	Indicates ROM data bit 7 through 10, 14 and 15
*BMD10	(RM2F24)	
BMD14	(RM2H34)	
*BMD14	(RM2H24)	
BMD15	(RM2K34)	
*BMD15	(RM2K24)	
BOEVN	(BI1LY4)	Indicates that Bus Out signal is even
BOT	(TK2A26)	
BOT	(SN1J44)	Indicates that beginning of tape is detected
BOTS	(BI1A54)	
BO0	(BO3B44)	
*BO0	(BO3B34)	Indicates individual contents of 8 bits from 0 to 7 of Bus Out signal form the FMT
BO1	(BO3E44)	

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Nickname	Pin-Index	Description
*BO1	(B03E34)	
BO2	(B03H44)	
*BO2	(B03H34)	
BO3	(B03L44)	Indicates individual contents of 8 bits from 0 to 7 of Bus Out signal from the FMT
*BO3	(B03L34)	
BO4	(B03B94)	
*BO4	(B03B84)	
BO5	(B03E94)	
*BO5	(B03E84)	
BO6	(B03H94)	
*BO6	(B03H84)	
BO7	(B03L94)	
*BO7	(B03L84)	
BRUN	(SN1G34)	Indicates that capstan motor is rotating backward
BUSEN	(B13Q34)	Indicates that Bus In and Bus Out are enabled
*BUSEN	(B13P24)	
BUS00	(RG1B24)	
BUS01	(RG1B25)	
BUS02	(RG1B26)	
BUS03	(RG1B27)	Indicates individual contents of bus bits of a register file
BUS04	(RG1C24)	
BUS05	(RG1C25)	
BUS06	(RG1C26)	

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Nickname	Pin-Index	Description
BUS07	(RG1C27)	
BUS10	(RG1B64)	
BUS11	(RG1B65)	
BUS12	(RG1B66)	
BUS13	(RG1B67)	
BUS14	(RG1C64)	
BUS15	(RG1C65)	
BUS16	(RG1C66)	
BUS17	(RG1C67)	
BUS20	(RG1BX4)	
BUS21	(RG1BX5)	Indicates individual contents of bus bits of a register file
BUS22	(RG1BX6)	
BUS23	(RG1BX7)	
BUS24	(RG1CX4)	
BUS25	(RG1CX5)	
BUS26	(RG1CX6)	
BUS27	(RG1CX7)	
BUS30	(RG2D24)	
BUS31	(RG2D25)	
BUS32	(RG2D26)	
BUS33	(RG2D27)	
BUS34	(RG2F24)	
BUS35	(RG2F25)	
BUS36	(RG2F26)	
BUS37	(RG2F27)	

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Nickname	Pin-Index	Description
BVLF	(TK2BX4)	Directs the drive of power amplifier for file reel
BVLM	(TK2CX6)	Directs the drive of power amplifier for machine reel
BWD	(RG1G25)	Indicates that the MTU is in the backward status
CAPGO	(RG1L25)	GO signal used in the MTU
CARY	(AL1GY2)	Indicates the content of carry bit of the calculated result in the arithmetic and logic unit (ALU)
CBI0	(BI2B54)	
CBI1	(BI2F54)	
CBI2	(BI2H54)	
CBI3	(BI2M54)	Indicates individual contents of 8 bits of Bus In signal to the FMT
CBI4	(BI2BX4)	
CBI5	(BI2FX4)	
CBI6	(BI2HX4)	
CBI7	(BI2MX4)	
*CBO0	(BOIB24)	
*CBO1	(BOIC24)	
*CBO2	(BOID24)	Indicates individual contents of 9 bits of Bus Out signal to the FMT
*CBO3	(BOIH24)	
*CBO4	(BOIJ24)	
*CBO5	(BOIK24)	

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Nickname	Pin-Index	Description
*CB06	(BOIB64)	
*CB07	(BOIC64)	
CB08	(BOID74)	Indicates individual contents of 9 bits of Bus Out signal to the FMT
*CB08	(BOID64)	
CCS	(TK2N26)	Indicates the magnitude of capstan motor current
CCTL	(B01J74)	Control tag signals from the FMT
*CCTL	(B01J64)	
CEN	(RW1N74)	Directs the drive of address counter
*CEON	(CE1J54)	Indicates that maintenance panel is connected to the MTU
CERST	(BO2P64)	Error Reset signal from the FMT
CGO	(B01K74)	
*CGO	(B01K64)	Go tap signal from the FMT
*CGOPN	(VQ1A24)	Cartridge open signal
CGRLY	(TK2A64)	Directs the drive of cartridge drive relay
*CLINF	(TK2D24)	Column In signal (File reel, machine reel)
*CLINM	(TK2D25)	
CLNDV	(RG1F64)	
*CLNDV	(CB1E34)	Auto cleaner drive signal
CLOCK	(RW5F24)	1-MHz clock

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
*CLR	(RM3N45)	Directs the MTU to clear its entire logical circuit network
CNTMD	(RG3H24)	Count mode signal
CNT1	(RW4B34)	Indicates the content of control counter specifying either serial or parallel control
CNT2	(RW4B35)	
CN0	(RG3N47)	
CN1	(RG3N46)	
CN2	(RG3N45)	
CN3	(RG3N44)	Indicates individual contents of 8 bits of a counter
CN4	(RG3K47)	
CN5	(RG3K46)	
CN6	(RG3K45)	
CN7	(RG3K44)	
CPA	(SN1C34)	Specifies either capstan tachometer A or B
CPB	(SN1D34)	
*CPLOK	(TK1C24)	Directs filter switching
CSF	(TK2PX4)	Indicates the magnitude of file reel motor current
CSL0	(RG1GX6)	Specifies the number of steps for counter
CSL1	(RG1GX7)	
CSM	(TK2NX4)	Indicates the magnitude of machine reel motor current

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Nickname	Pin-Index	Description
CSTS	(B01H74)	Status Tag signal from the FMT
*CSTS	(B01H64)	
CSVON	(TK1B34)	Makes servo control of capstan motor effective
*CSVON	(TK1B24)	
CTA	(SN1C52)	Capstan tach signal synchronized with clock signal
CTB	(SN1C56)	
CTGCL	(TK1H34)	Cartridge Close signal
CTGCL	(RG1E66)	
CTGON	(VQ1A23)	Cartridge On signal
CTL	(B03P84)	
CTLTG	(B02C54)	Control Tag signal from the FMT
CTPA	(TK2A23)	
CTPB	(TK2A24)	Capstan tacho signals A/B
C1024	(RG3N76)	Indicates that the contents of the upper bit of counter is 1024
C200U	(CL1J74)	Clock with a cycle of 200 μ s
C256	(RG3N74)	
C512	(RG3N75)	Indicates that individual contents of the upper bits of counter are 256, 512, 2048, 4095, respectively
C2048	(RG3N77)	
C4095	(RG3P74)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
DAC	(TK2N25)	Indicates the magnitude of output voltage of D/A converter
DAC1	(RG1K67)	
DAC2	(RG1K66)	
DAC4	(RG1K65)	Indicates individual contents of 5 bits to D/A converter
DAC8	(RG1K64)	
DAC16	(RG1J67)	
DBOB	(RW3F34)	Indicates that a data block is detected
DBUS0	(MX3B32)	
DBUS1	(MX3B34)	
DBUS2	(MX3B36)	
DBUS3	(MX3C32)	
DUBS4	(MX3C34)	Indicates individual contents of Data Bus signals to the arithmetic and logic unit (ALU)
DUBS5	(MX3C36)	
DUBS6	(MX3E32)	
DUBS7	(MX3E34)	
DDF	(RW4M64)	Indicates that the MTU is a dual-density unit
DEGUS	(RW1G84)	Degauss signal
DGO	(RW2L94)	Go signal
DIAG	(RW1EX5)	Indicates that the MTU is in diagnostic mode
*DIAG1	(TK1M24)	
DIBG	(RW3F33)	Indicates that IBG is detected

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Nickname	Pin-Index	Description
DNOIS	(RW3F35)	Indicates that a noise block is detected
*DOPEN DOPN	(CB1P66)	Indicates that door open is detected
	(CB1Q84)	
*DRCLS	(TK2E26)	Indicates that the door is closed
DSE	(RG1K25)	Indicates that the data security erase operation is being carried out
*DSPCL	(CL1G94)	
*DSPCL	(CL1GX4)	1-MHz clock
DTM	(RW3F32)	Indicates that tape mark block is detected
*DTUCK	(AA1C66)	Tape Unit Check Signal
*ECDCL	(MX3L64)	Directs the MTU to indicate an error code
ECDSP	(MX3N44)	
ECD0	(RG1L64)	
ECD1	(RG1L65)	
ECD2	(RG1L66)	
ECD3	(RG1L67)	Indicates individual contents of 8 bits of an error code
ECD4	(RG1M64)	
ECD5	(RG1M65)	
ECD6	(RG1M66)	
ECD7	(RG1M67)	
ECER	(RW1E64)	Indicates that the erase circuit fails

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Nickname	Pin-Index	Description
*ECHCL	(RW2CY4)	Clock for checking the erase circuit
ECON	(RW4G64)	Directs the MTU to put on the erase current
EC0	(BI4F94)	
EC1	(BI4E94)	Indicates individual contents of 4 bits of EC level signal
EC2	(BI4D94)	
EC3	(BI4C94)	
EMKDV	(RG1E65)	
*EMKDV	(TK1F34)	Error Marker Drive signal
EMKPW	(TK2A66)	
*EMMVD	(VQ1B23)	Directs the MTU to check the error marker operation
ENITR	(B14K94)	Indicates that interrupt is enabled
*ENLVL	(RW6D44)	Directs the MTU to set a slice level from an external unit
EOT	(SN1L44)	Indicates that the end of tape (EOT) marker is detected
EOT	(TK2B23)	
EQL1	(AL1D37)	
EQL2	(AL1D37)	Indicates that values compared through the execution of a compare instruction at the ALU are equal to each other
EQUAL	(AL1GY5)	
*ERON	(RW5E24)	Directs the MTU to set the erase current

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ERRST	(RG1H24)	
*ERRST	(AA1L34)	Directs the MTU to reset an error
*ERRST	(AA1J24)	
ERSC	(RG1LX6)	Directs the MTU to set the erase current
*ERSP	(B12P64)	Gate condition for sense information
ESLVO	(RW6B74)	
*ESLV1	(RW6C74)	
*ESLV2	(RW6D74)	
*ESLV3	(RW6E74)	Indicates individual contents of 8 bits of a slice level set signal from an external unit
*ESLV4	(RW6F74)	
*ESLV5	(RW6G74)	
*ESLV6	(RW6H74)	
*ESLV7	(RW4D85)	
*ESTDL	(RG4Q34)	Capstan tacho signal synchronized with clock signal
EVEN	(AL1M94)	Indicates the content resulting from parity checking for ROM data
*FCCWN	(TD1EX4)	Drives the file reel motor counter clockwise
FCCWP	(TD1FX4)	
*FCWN	(TD1CX4)	Drives the file reel motor clockwise
FCWP	(TD1BX4)	Drives the file reel motor clockwise
*FDRUN	(TK1D24)	Indicates that capstan motor is rotating in the forward direction

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Nickname	Pin-Index	Description
*FFRST	(AA1P94)	Directs the MTU to reset the unit check
FGO	(AA2F34)	Forward Go signal
FILE	(RG1FX7)	Indicates that the skip file operation is being executed
FP	(B1E34)	File Protect signal
FP	(RW5F55)	
FPK	(CB1N84)	Indicates the state of (PROTECT) switch on the operating panel
*FPK	(CB1DX6)	
FPLP	(CB1F84)	Turns on (PROTECT) LED
FRBWD	(RG5G94)	Indicates that the file reel is rotating backward when the MTU is in the servo off status
FRFWD	(RG5G92)	Indicates that the file reel is rotating in the forward direction
FRUN	(SN1F34)	Indicates that capstan motor is rotating in the forward direction
FTP-A	(RG2MY6)	
*FTPA	(SN1GY4)	Indicates that one cycle of this signal is equal to 4 QTP of capstan tachometer pulse
*FTPB	(SN1HY4)	
FVLF	(TK2BX#)	Directs the MTU to drive the power amplifier for file reel
FVLM	(TK2CX5)	Directs the MTU to drive the power amplifier for the machine reel

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
FW	(SN1C96)	Indicates that capstan motor is rotating in the forward direction
FWD	(SN1C96)	Forward Status signal
FWD	(BI2G24)	
GAPCT	(RG3J94)	Gap Control Signal
GAPEN	(RG1EX6)	Signal to control the gap control
GCR	(RW3E54)	Specifies the Group Coded Recording mode
GND	(RM3B24)	0 V
GO	(RW5H24)	
GO	(B12N24)	
GO	(B02D54)	Go signal
GO	(B02E64)	
GOB	(RW3C94)	
*HDR	(RW1K74)	Indicates that parity error occurs in the ROM or in the register file large-scale integration
*HIC	(RW3B74)	Directs the MTU to put on the high current
*HIC	(RB3B64)	
HID	(BI1E74)	Indicates that the MTU is in the high-density mode
HRDER	(RW1K84)	Indicates that parity error occurs in the ROM or in LSI

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
*HSBRK	(TK2LX3)	Indicates that file reel motor brake is applied during high-speed operation
HSC	(RG1LX7)	Indicates that the tape is running at a speed greater than 200 ips
HSCF	(TK2DX4)	Specifies the upper limit of capstan speed in response to the reel during high-speed rewinding
HSCM	(TK2DX5)	
HSMD	(RG1MX6)	Indicates that the tape is running in the high-speed mode
*HSP	(TD1A84)	This signal is kept in the on stage until the low tape is detected during rewinding
HSRUN	(RG1L24)	
HUBAL	(AA1G64)	
*HUBLK	(TK2D26)	Indicates that the auto hub has been locked
*INHCK	(AA1FX4)	Gate signal for the Alarm signals
INRST	(TK1C74)	Put on initial reset
*INRST	(TK1CX4)	
INSTL	(PG1EX4)	PCA Installed signal
INTRP	(AA2B74)	Interrupt signal to the FMT
*ISTLP	(VQ1C25)	Indicates that the PCA has been connected

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Nickname	Pin-Index	Description
*JMPOK	(MX3E36)	
*JUMP	(MX1KY4)	Conditional jump signal
KCNT	(CE1D56)	(CNT) switch on the maintenance panel
*KCNT	(CE1C56)	
KOFL	(CE1B56)	(OFL) switch on the maintenance panel
*KOFL	(CE1B54)	
KSSS	(CE1D54)	
*KSSS	(CE1C54)	(SSS) switch on the maintenance panel
KT	(RG5D92)	Capstan start signal
*LDCLK	(RW4B54)	Clock for setting the sense information sent from the read circuit network
LDCMD	(AL1H23)	LOAD instruction
LDFL	(BI1J74)	Signal to inform the MTU that tape loading fails
LDRK	(CB1J84)	Indicates the state of (LOAD REWIND) switch on the operating panel
*LDRK	(CB1FX6)	
LEDO	(MX3BY4)	
LED1	(MX3BY6)	
LED2	(MX3CY4)	
LED3	(MX3CY6)	
LED4	(MX3FY4)	Drives individual luminescent diodes for indicating the states of the corresponding switches on the maintenance panel

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Nickname	Pin-Index	Description
LED5	(MX3FY6)	
LED6	(MX3GY4)	
LED7	(MX3GY6)	
LED8	(MX3KY4)	Drives individual luminescent diodes for indicating the states of the corresponding switches on the maintenance panel
LED9	(MX3KY6)	
LED10	(MX3LY4)	
LED11	(MX3LY6)	
LINE1	(VQ1G24)	
LINE2	(VQ1F26)	
LINE3	(VQ1G25)	
LINE4	(VQ1P25)	Not used at present
LINE5	(VQ1P26)	
LINE6	(VQ1Q23)	
LINE7	(VA1Q25)	
LOAD	(RG1FX6)	Indicates that auto loading is being executed
LOCK	(RG1J64)	Signal to control the stop position of capstan
LSF	(TK2PX5)	Indicates the magnitude of capacitive sensor output voltage for the column on the file reel side
LSM	(TK2NX6)	Indicates the magnitude of capacitive sensor output voltage for the column on the machine reel side
LTLAL	(AA1H64)	Alarm signal for left column

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Nickname	Pin-Index	Description
LTP	(AA1D74)	Low tape detecting signal
LTPAS	(RG1L26)	
LVL0	(RG1J24)	
LVL1	(RG1J25)	
LVL2	(RG1J26)	
*LVL1	(RW2B82)	
*LVL16	(RW4D82)	
*LVL2	(RW4B83)	Specifies slice levels
*LVL32	(RW4D83)	
*LVL4	(RW4B84)	
*LVL64	(RW4D84)	
LVL65	(RG5P96)	
*LVL8	(RW4B85)	
LVL90	(RG5Q92)	
LWR	(RG5H42)	
*LWR	(RO2E44)	Loop Write-to-Read signal
LWRGO	(BO2M64)	
LWR2	(RG5H45)	
LWSL	(RG1LX4)	Low slice signal to direct the MTU to set the slice level to 7%
*LWSL	(RW4C54)	
LWTP	(TK2B24)	Indicates that low tape detected
*MARGN	(VQ1NY4)	Directs the MTU to execute the margin test

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Nickname	Pin-Index	Description
MASK	(RG1HX7)	Directs the MTU to inhibit interrupt to the microprogram
*MCCWN MCCWP	(TD1MX4) (TDINX4)	Directs the MTU to drive machine reel counterclockwise
*MCWN MCWP	(TD1KX4) (TD1JX4)	Directs the MTU to drive machine reel clockwise
MD00 MD01 MD02 MD03 MD04 MD05 MD06 MD07 MD08 MD09 MD10 MD11 MD12 MD13 MD14 MD15 MD16 MD17	(RM3NX4) (RM3MX4) (RM3LX4) (RM3KX4) (RM3JX4) (RM3HX4) (RM3GX4) (RM3FX4) (RM3EX4) (RM3DX4) (RM3CX4) (RM3BX4) (RM3N74) (RM3M74) (RM3L74) (RM3K74) (RM3J74) (RM3H74)	Indicates individual contents of 24 bits of memory data

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Nickname	Pin-Index	Description
MD18	(RM3G74)	
MD19	(RM3F74)	
MD20	(RM3E74)	Indicates individual contents of 24 bits of memory data
MD21	(RM3D74)	
MD22	(RM3C74)	
MD23	(RM3B74)	
MISCE	(RG5B54)	Miscellaneous Error signal to be set by the microprogram
MMET	(TK2B64)	
*MMET	(TK2B64)	Drives the moving meter
*MMTDV	(TK1N24)	
MOD	(AL1G26)	Control signal for the arithmetic and logic unit (ALU)
MPXA	(B02K64)	Specifies sense information for Bus In
MPXB	(B02L64)	
MPX06	(RM2C84)	
MPX07	(RM2B86)	Specifies the jump condition of register file
MPX08	(RM2B84)	
MPX09	(RM2C86)	
MPX1	(BI1KX2)	Specifies sense byte
MPX10	(RM2M34)	Specifies the jump condition of register file

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
MPX2	(B11KX3)	Specified sense byte
MPX4	(B11KX4)	
MRBWD	(RG5H92)	Signal issued when the MTU is in the servo off status to direct the MTU to rotate the machine reel in the backward direction
MRFWD	(RG5G96)	Signal issued when the MTU is in the servo off status to direct the MTU to drive machine reel in the forward direction
*MRSIW	(TK2K94)	Directs the MTU to drive machine reel motor at low speed
*M6ALM	(VQ1C23)	Alarm signal for the -6 V stabilizing circuit
NCLF	(TK2AX4)	Directs the MTU to drive the power amplifier for the file reel
NCLM	(TK2CX3)	Directs the MTU to drive the power amplifier for machine reel
NEWF	(B14H94)	Specifies the MTU having the streaming function for the skip file function
NRZI	(B02J54)	Sets the MTU in the Non Return to Zero Change on One (NRZI) mode
OD	(SN1CX4)	Indicates that capstan runs in the direction opposite the Status signal
ODD	(AL1M96)	Indicates the content resulting from parity checking from ROM data

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ONL	(AA1C62)	Indicates that the MTU is in the online status
*ONL	(AA1C63)	
ONLK	(CB1K84)	Indicates the state of (ONLINE) switch on the operating panel
*ONLK	(CB1EX5)	
OPTN0	(MX2M34)	
OPTN1	(MX2N34)	Indicates the content of the specified option
OPTN2	(MX2P34)	
OPTN3	(MX2Q34)	
OVCF	(TK2AX6)	Specifies the upper limit of the maximum current to file reel motor
OVCM	(TK2CX4)	Specifies the upper limit of the maximum current to machine reel motor
PCLF	(TK2KX5)	Directs the MTU to drive the power amplifier for file reel
PCLM	(TK2BX5)	Directs the MTU to drive the power amplifier for machine reel
PDTO	(RG1JX4)	
PDT1	(TG1JX5)	
PDT2	(RG1JX6)	Preset data signals for Timer 0
PDT3	(RG1JX7)	
PDT4	(RG1KX4)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
PDT5	(RG1KX5)	
PDT6	(RG1KX6)	Preset data signals for Timer 0
PDT7	(RG1KX7)	
PE	(RG1H27)	Sets the MTU in the Phase Encode mode
POWAL	(CB1B54)	Turns on (ALARM) lamp on the operating board
PRSET	(RG5Q96)	Directs the MTU to reset the counter for DGC amplifier control
PRSVL	(RG1F66)	Drives the pressure valve
PTYCL	(CL1B94)	1-MHz clock
PWRDY	(TK2B25)	Indicates that power supply is ready
PWRDY	(TK1C64)	
P1US	(CL1C66)	Clock to divide a frequency of 8 MHz
P25ON	(CL1C64)	
*P5ALM	(VQ1B24)	Alarm signal for ± 5 V stabilizing circuit
P500N	(CL1C65)	Clock to divide a frequency of 8 MHz
QTP	(SN1F74)	Quarter Tacho Pulse signal
RAMON	(RM1Q74)	Indicates that the RAM is connected to an external device
*RAMON	(RE3E47)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
RD	(SN1H24)	Read Status signal
RDINH	(RG5P92)	Directs the MTU to inhibit sending the read data
RDST	(RW1E24)	Indicates that the MTU is in the read status
RDT0	(RW5M54)	
RDT1	(RW5L56)	
RDT2	(RW5L54)	
RDT3	(RW5K56)	
RDT4	(RW5K54)	Indicates individual contents of 9 bits of read data
RDT5	(RW5J56)	
RDT6	(RW5J54)	
RDT7	(RW5H56)	
RDT8	(RW5H54)	
RDYHL	(RG5G42)	Indicates that ready signal to set
RDYLP	(CB1B84)	Directs the MTU to turn on (READY) lamp on the operating panel
READ	(BI2P34)	Output gate signal for read data
READ	(BI2N34)	
READY	(RG1EX7)	Indicates that the MTU is in the ready status
*RESET	(RW5L24)	Directs the MTU to result the counter for the DGC amplifier control

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
RGB0	(MK1#84)	
RGB1	(MK1F84)	
RGB2	(MK2E24)	
RGB3	(MK2F24)	Indicates individual contents of 8 bits of output bus of register file large-scale integration (LSI)
RGB4	(MX2E64)	
RGB5	(MX2F64)	
RGB6	(MX2EX4)	
RGB7	(MX2FX4)	
RGPE	(RW1L64)	
RGPE0	(RG1N24)	Indicates individual contents of parity errors in register file LSI
RGPE1	(RG1N64)	
RGPE2	(RG1NX4)	
*RGW	(RG2K34)	Directs the MTU to write the content of general register
RLACT	(RG1J27)	Indicates that capstan is in operation
*RLPWR	(TK1Q74)	Directs the MTU to make the reel motor drive mechanism effective
*RLSLW	(TK11P74)	Directs the MTU to drive file reel motor at low speed
RLSTP	(RG1M26)	Indicates that reel motor is in halt
*RLSTP	(TK1K24)	
*RNOIS	(RG4G34)	Directs the MTU to reset the Noise Block Detect signal
ROMCK	(RW1N64)	Parity error signal for the ROM

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ROMOK	(RW1Q64)	
*ROMOK	(RW1Q66)	Parity OK signal for the ROM
ROMPE	(RW1K64)	Parity error signal for the ROM
ROM00	(RM1N85)	
ROM01	(RM1N84)	
*ROM02	(RM1P94)	
ROM03	(RM1N82)	
ROM03	(RM1NY4)	
ROM04	(RM1M85)	
ROM05	(RM1M84)	
ROM05	(RM1LY4)	
ROM06	(RM1M83)	
ROM07	(RM1M82)	
ROM08	(RM1H85)	
ROM09	(RM1H84)	
ROM10	(RM1H83)	Indicates individual contents of 24 bits of ROM data.
ROM11	(RM1H82)	
ROM12	(RM1G85)	
ROM13	(RM1G84)	
ROM14	(RM1G83)	
ROM15	(RM1G82)	
ROM16	(RM1C85)	
ROM17	(RM1C84)	
ROM18	(RM1C83)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
ROM19	(RM1C82)	
ROM20	(RM1B85)	
ROM21	(RM1B84)	Indicates individual contents of 24 bits of ROM data.
ROM22	(RM1B83)	
ROM23	(RM1B82)	
RPWR	(RG5H94)	Directs the MTU to make the reel motor drive mechanism effective
RSPB	(AA1N94)	
RSTK	(CB1M84)	Indicates the state of (RESET) switch on the operating panel
*RSTK	(CB1EX6)	
RSTKS	(AA1M82)	
RSVSW	(RG1M27)	Directs the MTU to make servo control of reel motor effective
*RSVSW	(TK1L24)	
RTLAL	(AA1J64)	Alarm signal for right column
*RTSFL	(RG4P34)	Reset signal upon completing the maintenance panel operation
*RUCHL	(RG4H34)	Directs the MTU to reset the unit check signal
RVSL	(RG1L27)	Indicates that a predetermined time passes after capstan motor stopped
RWD	(RG1K26)	Indicates that the rewind operation is in execution
SAGC	(RG5M96)	Indicates that the MTU is in the Self-Adjust Gain Control mode

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
SAGCO	(RW4K62)	
SAGC1	(RW4J66)	
SAGC2	(RW4J64)	Indicates individual contents of 4 bits of count information in AGC counter
SAGC3	(RW4J62)	
*SBI0	(B11B36)	
*SBI1	(BK1F36)	
*SBI2	(B11K36)	
*SBI3	(B11B66)	Indicates individual contents of 8 bits of sense byte
*SBI4	(B11F66)	
*SBI5	(B11K66)	
*SBI6	(B11B96)	
*SBI7	(B11F96)	
SBOT	(SN1D54)	Indicates that beginning of tape (BOT) is detected
*SBYT	(B13G64)	Gate signal for the sense byte output signal
SCONT	(RW5G24)	Directs the MTU to control the serial/parallel transmission
SDF	(TK2PX6)	Directs the MTU to drive file reel
SDM	(TK2NX3)	Directs the MTU to drive machine reel
SECON	(RW4Q34)	Directs the MTU to set the erase current on
SEMK	(RG1K24)	Directs the MTU to stamp the error mark

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
*SENS	(B02F64)	Gate signal for the sense byte output
SEOT	(SN1D56)	Indicates that end of tape (EOT) is detected
SERS	(RG1G24)	Indicates that the MTU is in the erase mode
SET	(RG5Q94)	Directs the MTU to inhibit the operation of the counter for the DGC amplifier control
*SET	(RW5N24)	
SETP2	(RG4K34)	
SETP3	(RG4134)	Pulse signals for setting
SETP5	(RG4N34)	
SFBLS	(CB1P84)	Indicates whether or not the window safety mechanism operates (* = not operational)
*SFBLS	(TK2L63)	
SHBOT	(RG1FX5)	Indicates that the processing for detecting the BOT is in execution
*SIGN	(RG1J66)	Signal for switching the polarity of D/A converter
SIN	(RW4KY4)	Indicates the content of the parallel serial transmission of data to the write/read PCA
SIRPT	(RG4M34)	Indicates the condition for setting an interrupt signal

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
SKIPF	(BI4J94)	Indicates that the MTU is provided with the skip file function
SLOW	(RG5H96)	Directs the MTU to drive the reel at low speed
*SLS0 *SLS1	(MX1L34) (MX1M34)	Specifies the jump condition for register file
SOUT	(RW5F57)	Indicates the parallel/serial transmission data from write/read PCA
SPOS	(RG1MX4)	Positioning control signal for capstan
*SSSON	(CE1LX4)	Indicates the state of (SSS) switch on the field tester
SSTEP *SSTEP	(RG5P94) (RW5P24)	Clock signal for the counter for the DGC
STB10 STB11 STB12 STB13 STB14 STB15 STB16 STB17	(BI5DY4) (BI5DY6) (BI5EY4) (BI5EY6) (BI5HY4) (BI5HY6) (BI5JY4) (BI5JY6)	Indicates individual contents of 8 bits of the Bus In signal to be issued when the MTU receives the Status Tag signal
STEP6	(RW4F62)	Indicates that the number of the steps of the counter for the DGC amplifier control is 6

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
STPCK	(RG5D94)	Directs the MTU to check the position control when capstan stops
STRMD	(RG1FX4)	Directs the MTU to set the streaming mode
	(RG1H25)	
STRMF	(RW4K64)	Indicates that the MTU is provided with the streaming mode function
*STRST	(AA2D64)	Directs the MTU to reset the error detecting circuit
STS	(B02B54)	Status Tag signal
SUCHL	(AALCX4)	Sets the unit check holding signal
SVOK	(RG1M24)	
*SVOK	(SN1P44)	Directs the MTU to set reel/capstan motor in the servo control state
SVON	(RG1M25)	
SWCER	(RW1E74)	Alarm signal for the write and erase circuits
SWCON	(RW4P34)	Directs the MTU to set the magnitude of the write voltage
SWRS	(RG1G27)	Indicates the write status
*SWRS	(RS1G74)	
SWO	(CE1NY6)	
SWO	(MX3H94)	
*SWO	(CE1NY5)	Indicates individual contents of 8 switches 0-7 on the field tester
*SWO	(CE1H54)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
SW1	(CE1NY3)	
*SW1	(CE1NY2)	
*SW1	(CE1H56)	
SW2	(CE1MY6)	
*SW2	(CE1G56)	
SW3	(CE1MY3)	
*SW3	(CE1G54)	
SW4	(CE1KY6)	Indicates individual contents of 8 switches 0-7 on the field tester
*SW4	(CE1F56)	
SW5	(CE1KY3)	
*SW5	(CE1F54)	
SW6	(CE1JY6)	
*SW6	(CE1E56)	
SW7	(CE1JY3)	
*SW7	(CE1E54)	
S0	(AL1G22)	
S1	(AL1G23)	Signals for arithmetic and logic unit (ALU) control
S2	(AL1G24)	
S3	(AL1G25)	
*TAGIN	(B13F83)	Indicates that Tag signals from the FMT are normal
TEST	(CE1QX4)	Indicates the content of (SSS) switch on the field tester
TGPE	(BO2B34)	Indicates the content resulting from parity check for Tag and Bus out
*TGPE	(BO2B36)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
*THALM	(VQ1C24)	Termpreature alarm signal for power supply circuit
TID0	(BI4K64)	
TID1	(BI4J64)	
TID2	(BI4H64)	
TID3	(BI4G64)	Indicates individual contents of 13 bits for unit number setting
TID4	(BI4F64)	
TID5	(BI4E64)	
TID6	(BI4D64)	
TID7	(BI4C64)	
TID8	(BI4K34)	
TID9	(BI4J34)	
TID10	(BI4H34)	
TID11	(BI4G34)	
TID12	(BI4F34)	
*TLV1	(RW6B34)	
*TLV16	(RW6C35)	
*TLV2	(RW6B36)	Indicates individual contents of 7 bits for slice level setting from an external device
*TLV32	(RW6C36)	
*TLV4	(RW6B37)	
*TLV64	(RW6C37)	
*TLV8	(RW6C34)	
TM	(RG5D45)	Indicates that a tape mark block is detected

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
TMOD	(RG5G45)	Indicates that the MTU is in the test mode
TMOV0	(RG2MY4)	Indicates that timer overflows
TMOV1	(RG3E94)	
TMSR0	(RW5M57)	
TMSR1	(RW5M55)	
TMSR2	(RW5L57)	
TMSR3	(RW5K57)	
TMSR4	(RW5K55)	
TMSR5	(RW5J57)	Indicates that the amplitude of the read data reaches a predetermined value
TMSR6	(RW5J55)	
TMSR7	(RW5H57)	
TMSR8	(RW5G57)	
TM00	(RG2KY6)	
TM01	(RG2KY4)	
TM02	(RG2JY6)	
TM03	(RG2JY4)	
TM04	(RG2DY6)	Indicates individual contents of 8 bits of timer 0
TM05	(RG2DY4)	
TM06	(RG2CY6)	
TM07	(RG2CY4)	
TM10	(RG3G47)	
TM11	(RG3G46)	Indicates individual contents of 8 bits of timer 1
TM12	(RG3G45)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
TM13	(RG3G44)	
TM14	(RG3D47)	Indicates individual contents of 8 bits of timer 1
TM15	(RG3D46)	
TM16	(RG3D45)	
TM17	(RG3D44)	
TOVRN	(RG5E42)	Indicates that no block is detected within a predetermined distance
TP	(SN1K34)	Indicates that the tape is present in the tape transmission mechanism
TSB0	(MX4B34)	
TSB1	(MX4F34)	
TSB2	(MX4K34)	
TSB3	(MX4B74)	
TSB4	(MX4F74)	Indicates individual contents of 8 bits of register files or test signals
TSB5	(MX4K74)	
TSB6	(MX4BY4)	
TSB7	(MX4FY4)	
*TSCL	(RW2B74)	Clock for write data composition
TSFL	(CE1M95)	Indicates that the maintenance panel is in use
TSL00	(RG1HX4)	
TSL10	(RG1GX4)	Specifies the number of steps for timer 0 and 1
TSL11	(RG1GX5)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
TST *TST	(CE1M92) (CE1M93)	Indicates the state of (SSS) switch on the field tester
TUCHK TUCK TUERR	(AA1F84) (BI5G94) (AA1E64)	Indicates that failure of MTU is detected
TWA	(SN1L64)	Indicates that end of tape (EOT) is detected when the tape runs in the forward direction
TWRCL TWRDT	(RW2GY4) (RW2F94)	Write clock and write data from the maintenance panel
T1US	(CL1D74)	Pulse signal with a cycle of 1 μ s
TI6US	(RW4C34)	Pulse signal with a cycle of 16 μ s
U-SPO U-SP1 U-SP2 *USP3	(RG4C34) (RG4D34) (RG4E34) (RG4F34)	Indicates individual contents of the upper 4 bits of the Set Pulse signal
UCHLD *UCHLD	(AA1DX4) (AA1DX6)	Directs the MTU to set the unit check signal in the holding state
UCK UCK	(AA1B94) (AA1E84)	Indicates that failure of the MTU is detected

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
UCKLP	(CB1D84)	Directs the MTU to turn on (UNIT CHECK) lamp
UCKLP	(RG1E64)	
UNL	(RG1E64)	Indicates that unloading is in execution
UNLK	(CB1L84)	Indicates the state of (UNLOAD) switch on the operating panel
UNLK	(CB1FX4)	
*VALVA	(CB1B34)	Directs the MTU to drive the valve
*VALVB	(CB1C34)	
VELO	(RW4K66)	Specifies the unit speed
VEL1	(RW4M62)	
VSF	(TK2NX5)	Indicates the detected magnitude of the file reel motor voltage
VSM	(TK2MX5)	Indicates the detected magnitude of the machine reel motor voltage
V0	(RM3F44)	
V1	(RM3F45)	
V10	(RM3H47)	
V11	(RM3H47)	
V12	(RM3J45)	Indicates individual contents of 24 data bits issued when the RAM is connected with an external device
V13	(RM3J46)	
V14	(RM3J47)	
V15	(RM3K44)	
V16	(RM3K45)	
V17	(RM3K46A)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
V18	(RM3K47)	
V19	(RM3L44)	
V2	(RM3F46)	
V20	(RM3L46)	
V21	(RM3L47)	
V22	(RM3M44)	
V23	(RM3M45)	Indicates individual contents of 24 data bits issued when the RAM is connected with an external device
V3	(RM3F47)	
V4	(RM3G44)	
V5	(RM3G45)	
V6	(RM3G46)	
V7	(RM3G47)	
V8	(RM3H44)	
V9	(RM3H46)	
WCER	(RW1D64)	Indicate failure in the write circuit
*WCHCL	(RW1BY4)	Clock signal for checking the write operation
*WDCLS	(TK2K65)	Indicates that the window has been closed
WDT0	(RW2K34)	
WDT1	(RW2K36)	
WDT2	(RW2L34)	Indicates individual contents of 9 bits of write data
WDT3	(RW2L36)	
WDT4	(RW2K64)	
WDT5	(RW2K66)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
WDT6	(RW2L64)	
WDT7	(RW2L66)	
WDT8	(RW2K94)	Indicates individual contents of 9 bits of write data
WKSL0	(RG1HX5)	Specifies a work register
WKSL1	(RG1HX6)	
WK00	(RG1E24)	
WK01	(RG1E25)	
WK02	(RG1E26)	
WK03	(RG1E27)	
WK04	(RG1F24)	Sets individual bits of the work register
WK05	(RG1F25)	
WK06	(RG1F26)	
WK07	(RG1F27)	
WK580	(RG4C84)	
WK581	(RG4C85)	
WK582	(RG4C86)	
WK583	(RG4C87)	
WK584	(RG4F84)	Sets individual bits of the work register
WK585	(RG4F85)	5-8
WK586	(RG4F86)	
WK587	(RG4F87)	
WNDCL	(RG1E67)	Directs the MTU to close the window
*WNDCL	(TK1P24)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
WPOS	(RG1MX5)	Directs the MTU to carry out positioning control on the write operation
WRCL	(B01BY4)	Write Clock signal
*WRIST	(RW4F64)	Indicates that the write/read PCA has been connected
WRTC	(RG1LX5)	Write voltage setting signal
WRTCL	(RW2K96)	Write clock signal
*WSEL0	(RM2F83)	
*WSEL1	(RM2F84)	
*WSEL2	(RM2F85)	
*WSEL3	(RM2F86)	
*WSEL5	(RM2G83)	
*WSEL6	(RM2G84)	
*WSL40	(RM2H94)	
*WSL41	(RM2H95)	Indicates individual contents of bits to direct the MTU to specify and set registers
WSL42	(RG3G74)	
*WSL42	(RM2H96)	
*WSL43	(RM2H97)	
WSL50	(RG4J24)	
*WSL50	(RM2J94)	
*WSL51	(RM2J95)	
*WSL52	(RM2J96)	
*WSL53	(RM2J97)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
*WSL60	(RG5B24)	
*WSL61	(RG5B25)	
*WSL62	(RG5B26)	Indicates individual contents of bits to direct the MTU to specify and set registers
*WSL63	(RG5B27)	
*WTCLK	(RW5C24)	Write Clock signal
WTOK	(RW5A44)	Indicates that the MTU is in the write voltage
*WTOK	(RW5B24)	
*WTON	(RW5D24)	Directs the MTU to set the write voltage
WVON	(RW4G62)	Directs the MTU to set the write voltage on
WVON	(RW4G84)	
XBUS0	(MX1B94)	
XBUS1	(MX1J94)	
XBUS2	(MX2B34)	
XBUS3	(MX2J34)	Indicates individual contents of 8 bits of register files or test signals
XBUS4	(MX2B74)	
XBUS5	(MX2J74)	
XBUS6	(MX2BY4)	
XBUS7	(MX2JY4)	
YB10	(BI3B34)	
YB11	(BI3C34)	
YB12	(BI3D34)	Indicates individual contents of 9 bits of the Bus In signal to the FMT
YB13	(BI3J34)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
YB14	(BI3K34)	
YB15	(BI3L34)	
YB16	(BI3BX4)	Indicates individual contents of 9 bits of the Bus In signal to the FMT
YB17	(BI3CX4)	
YB18	(BI3DX4)	
YB00	(BO2JX4)	
YB01	(BO2JX6)	
YB02	(BO2JX7)	
YB03	(BO2KX4)	Indicates individual contents of 9 bits of the Bus Out signal from the FMT
YB04	(BO2KX6)	
YB05	(BO2KX7)	
YB06	(BO2LX4)	
YB07	(BO2LX6)	
YB08	(BO2LX7)	
YCPA	(BI3K74)	Capstan Tacho signals A/B to the FMT
YCPB	(BI3L74)	
YCTL	(BO2MX7)	Control Tag Signal from the FMT
YGAPC	(BI3JX4)	Gap Control signal to the FMT
YGO	(BO2NX4)	Go signal from the FMT
YINT	(BI3J74)	Interrupt signal to the FMT
YRSV	(BO2HX4)	Reserve and Reserved signals from the FMT

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
YSTS	(B02MX6)	Status Tag singnal from the FMT
YWCL	(B02NX6)	Write Clock signal from the FMT
Y0	(RG2G64)	
Y1	(RG2G66)	
Y2	(RG2H64)	
Y3	(RG2H66)	Indicates individual contents of 8 bits of ROM data or general register 0
Y4	(RG2L64)	
Y5	(RG2L66)	
Y6	(RG2M64)	
Y7	(RG2M66)	
ZTH	(B02H64)	Directs the MTU to set the slice level of read amplifier to "0"
ZTRP	(AA1JY4)	Interrupt signal to the microprogram
Z00D	(B02M84)	
Z00D	(B01QY4)	
Z00D	(BI4QX4)	Indicates that the MTU is in the status of logical "0"
Z00D	(AL1QY4)	
Z00D	(RG1QX4)	
Z00D	(RG5QY4)	
Z01D	(RG5PY4)	
Z01D	(AL1PY4)	
Z01D	(BI4PX4)	
Z01D	(BI3QX4)	Indicates that the MTU is in the status of logical "1"
Z01D	(B01PY4)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
Z01F	(AA1NY4)	Indicates that the MTU is in the status of logical "1"
Z01F	(RW4PX4)	
0	(RW4NX4)	
0	(RW5Q24)	
0	(RW2NY4)	
0	(RG3QY4)	
0	(RG2QY4)	
0	(RM1QY4)	Indicates that the MTU is in the status of logical "0"
0	(AA1QY4)	
0	(B02Q84)	
0	(B11QY4)	
0	(MX2PY4)	
0	(MX1QY4)	
0	(MX3PY4)	
0V	(CB1E94)	
0V	(CB1MY4)	0 V
0V	(PW6Q24)	
1	(RW1QY4)	
1	(RW4MX4)	
1	(SN1QY4)	
1	(RG3PY4)	Indicates that the MTU is in the status of logical "1"
1	(CL1PY4)	
1	(MX2QY4)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
+13V	(VQ1A64)	
+13V	(VQ1A54)	
+13V	(VQ1A44)	+13 V
+13V	(TK2A94)	
+13V	(TK2A84)	
+5TST	(VQ1NY3)	
+5V	(TK2G65)	+5 V
+5V	(TK2H54)	
+5V	(CB1C94)	
-13V	(TK2Q84)	
-13V	(TK2Q94)	
-13V	(VQ1Q65)	-13 V
-13V	(VQ1Q54)	
-13V	(VQ1Q44)	
*12ALM	(VQ1B25)	Alarm signal for +12 V stabilizing circuit
12VOV	(RW4F66)	-12 V overcurrent is detected
16QTP	(SN1H94)	Capstan Tacho signal
1600	(RG1H26)	Indicates that the MTU is in the 1600 bpi mode
200US	(CL1L64)	
200US	(CL1K64)	Pulse signal with a cycle of 200 μ s
200US	(CL1L54)	

APPENDIX B SIGNALS IN LOGIC CIRCUITS

Nickname	Pin-Index	Description
3.2MS	(CL1P44)	Pulse signal with a cycle of 3.2 ms
3.2MS	(CL1P34)	
4US	(RW4A44)	Pulse signal with a cycle of 4 us
500K	(CL1D64)	Pulse signal with a cycle of 500 KHz
51MS	(CL1P64)	Pulse signal with a cycle of 51 ms
51MS	(CL1P54)	
6250F	(RW4M84)	Indicates that the MTU is provided with the function of 6250 bpi mode
6250F	(RW4M66)	
*75IPS	(TK1Q24)	Indicates that the MTU is running at 75 ips